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ond to a collection of information unless it contains a valid QMB control number, titute for form 1449A/PTO INFORMATION DISCLOSURE Complete if Known **Application Number** 10/813,226 STATEMENT BY APPLICANT March 30, 2004 **Filing Date** First Named Inventor Honary, Hooman 2183 **Art Unit** Unknown **Examiner Name** (Use as many sheets as necessary) Attorney Docket No. 80107.115US1 Sheet

US PATENT DOCUMENTS Examiner Cite USP Document Number Publication Date Name of Patentse or Applicant of cited Filing Date						
Examiner Initial *	Cite No	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	If Appropriate	
		US-20040194048A1	09/30/2004	Arnold, J. M.	03/31/2003	
		US-20050149890A1	07/07/2005	Tsai, V. W., et al.	12/29/2003	
		US-20050193357A1	09/01/2005	Honary, H. et al.	02/27/2004	
		US-20050229139A1	10/13/2005	Tsai, V. W., et al.	03/30/2004	
		US-20050229140A1	10/13/2005	Tsai, V. W., et al.	03/31/2004	
		US-5128871	07/07/1992	Schmitz, N. A.	03/07/1990	
		US-6038386	03/14/2000	Jain, G.	08/21/1997	
		US-6112023	08/29/2000	Dave, B. P., et al.	02/17/1998	
		US-6195788	02/27/2001	Leaver, A. et al.	10/09/1998	
		US-6226735	05/01/2001	Mirsky, E. A.	05/08/1998	
		US-6298319	10/02/2001	Heile, F. B., et al.	08/26/1999	
		US-6941538	09/06/2005	Hwang, L. J., et al.	02/22/2002	
·		US-7073159	07/04/2006	Tsai, V. W., et al.	03/31/2004	

	FOF	REIGN PATENT I	DOCUMENTS	
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T²
	WO-05094016A2	10/06/2005	Honary, H., et al.	
	WO-2005094016A3	10/06/2005	Honary, H.	

EXAMINER	/Sai Ming Chan/	DATE CONSIDERED	12/17/2008

PTO/SB/08a(07-0 Approved for use through 7/31/2006, OMB 0651-00: IS Patent & Trademark Office: U.S. DEPARTMENT OF COMMERC

Substitute	e for form 1449A/	РТО				
INFORMATION DISCLOSURE				Complete if Known		
STAT	EMENT B	Y APPLICA	NT	Application Number	10/813,226	
				Filing Date	March 30, 2004	
				First Named Inventor	Honary, Hooman	
				Art Unit	2183	
				Examiner Name	Unknown	
(L	Jse as many shee	ets as necessary)				
Sheet	2	of	2	Attorney Docket No: 80107.115US1		

	OTHE	R DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
		International Search Report and Written Opinion of the International Searching Authority; Dated 12/06/2005; PCT/US2005/010387, 16 pgs.	
		ALBANESI, M. G., et al., "A VLSI 128-Processor Chip for Multiresolution Image Processing", Proceedings of the First International Conference on Ischia, (May 02, 1994), 296-307 pgs.	
		MARESCAUX, T., et al., "Networks on Chip as Hardware Components of an OS for Reconfigurable Systems", FIELD-PROGRAMMABLE LOGIC AND APPLICATIONS, 13TH INTERNATIONAL CONFERENCE, Vol. 2778, (2003), 595-605 pgs.	
		ZHANG, et al., "Interconnect Architecture Exploration for Low-Energy Reconfigurable Single-Chip DSPs", VLSI, 99, PROCEEDINGS, IEEE COMPUTER SOCIETY WORKSHOP ON ORLANDO, FL, (April, 08, 1999), 2-8 pgs.	

/Sai Ming Chan/ 12/17/2008
EXAMINER DATE CONSIDERED